L Number	Hits	Search Text	DB	Time stamp
1	63	712/35.ccls. and (MAC or (multiply adj accumulate))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/23 11:10
2	24	712/35.ccls. and ((MAC or (multiply adj accumulate)) adj unit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/23 15:30
3	94	cache with (enabl$5 or disabl$5) with loop$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/23 15:31
-	246	712/225.ccls. and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 07:59
-	320	712/228.ccls. and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 14:00
-	292	712/207.ccls. and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 11:58
-	753	((operand or instruction) with (pre adj fetch$4)) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 14:15
-	607	(((operand or instruction) with (pre adj fetch$4)) and @ad<=20001120) and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 09:39
-	329	(((operand or instruction) with (pre adj fetch$4)) and @ad<=20001120) and pointer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 13:04
-	128	(((operand or instruction) with (pre adj fetch$4)) and @ad<=20001120) and disable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 14:05
-	75	(((operand or instruction) with (pre adj fetch$4)) and @ad<=20001120) and dirty	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 14:05
-	8	mar adj instruction and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 07:49
-	44	touch adj instruction and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 14:37
-	5	coefficient adj data adj pointer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:35

–	247	modification adj track$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 15:14
–	33	page adj file and dirty and @ad<20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/29 16:10
–	2545	(memory adj address adj register) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 08:30
–	951	((memory adj address adj register) and @ad<=20001120) and fetch$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 07:51
–	1	(((memory adj address adj register) and @ad<=20001120) and fetch$4) and oeprand	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 11:58
–	392	((memory adj address adj register) and @ad<=20001120) and fetch$4 and MAR	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 07:51
–	317	(cache adj invalidate) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 08:33
–	13	(cache adj invalidate adj instruction) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 08:33
–	2595	(base adj register) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 13:21
–	517	(((memory adj address adj register) and @ad<=20001120) and fetch$4) and operand	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 11:58
–	742	((base adj register) and @ad<=20001120) and stack and pointer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 13:20
–	2081	(indirect near address$4) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 13:21
–	323	(indirect near address$4 near mode) and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 13:22
–	329	712/227.ccls. and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 15:37

C:\APPS\EAST\Workspaces\BJO-09716493.wsp

-	143	(712/227.ccls. and @ad<=20001120) and debug$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 14:02
-	26	array near bounds near checking and @ad<=20001120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 15:38
-	13	712/205.ccls. and redundant	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 08:01
-	2	(inhibit or prevent or cancel) near3 (redundant or unneccessary or repetitive) near3 $fetch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 08:23
-	3	(inhibit or prevent or cancel) with (redundant or unneccessary or repetitive) with $fetch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 08:32
-	1	inhibit adj redundant adj fetch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 08:24
-	232	712/205.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 08:25
-	13	(inhibit$3 or prevent$3 or cancel$3) with (redundant or unneccessary or repetitive) with $fetch$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 08:40
-	3944	operand with (prefetch$3 or fetch$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 10:50
-	425	operand adj cache	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 12:44
-	425	"operand cache"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 10:53
-	175	711/123.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:09
-	181	712/35.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/23 11:08
-	96	712/35.ccls. and operand	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:33

-	4192	(data or operand) adj pointer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:35
-	868	((data or operand) adj pointer) and cache	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:36
-	469	((data or operand) adj pointer) and cache and fetch$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:36
-	44	((data or operand) adj pointer) same cache same fetch$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/22 14:36

C:\APPS\EAST\Workspaces\BJO-09716493.wsp